



## **Integre Technologies Adds HyperLynx® Tool**

*Signal Integrity Tool Improves Reliability of High Speed PCB Designs*

**Rochester, NY – September 1, 2011** – Integre Technologies, a leading provider of FPGA and PCB design services today announced the addition of the Mentor Graphics® HyperLynx® Signal Integrity Tool to their Design Center tool suite.

The HyperLynx tool enables the Integre team to accurately analyze and eliminate signal integrity problems. PCB signal integrity analysis and simulation is a critical part of high speed design, including SERDES applications. Integre is committed to providing customers with the highest quality designs by investing in tools and processes leading to first pass success.

### **About Integre**

Integre Technologies is an engineering services and product company specializing in digital and mixed-signal design and verification. Integre's capabilities include: FPGA and ASIC design and verification including multi-million gate SOC's, Mixed Signal ASIC Design and Circuit Board Design and Layout. Integre delivers solutions to both the commercial and mil/aero/secure communities, engineering ground, air and space based products. Integre is headquartered in Rochester, New York. For more information on Integre please visit our web site at [www.integretek.com](http://www.integretek.com).

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